



IN THE SPECIFICATION

Please replace the third paragraph beginning on page 5 line 21 with:

A1 Different CHT's are capable of representing predictions for store instructions in different ways. In implicit-predictor type of CHT 250 illustrated in **Figure 4**, all entries in CHT 250 implicitly correspond to store instructions predicted to be colliding. In full and tagless CHT's, illustrated in **Figures 3 and 5**, ~~respectively~~respectively, each entry has a respective predictor. The predictor bit is a bit that may be in a first or a second state. If the predictor bit is in the first state, the corresponding store instruction is predicted to be silent. If the predictor bit is in the second state, the corresponding store instruction is predicted to not be silent.

RECEIVED

FEB 19 2004

Please replace the first full paragraph on page 9 with the following:

Technology Center 2100

A2 After the silent store instructions are predicted, future load instructions can bypass the issued silent store instructions. For recovery purposes, the load instructions that are to bypass the silent store instructions are marked as bypassing. In one embodiment, the marking of the bypassing load is accomplished by the setting of one bit of the load predict portion in extended load buffer 1100 that is illustrated in **Figure 11**. Extended load buffer 1100 includes load data segment 1150, load address match segment 1140, load data match segment 1130, load predict segment 1120, load flush segment 1110, load address segment 1105 and load attribute segment 1106. In another embodiment, the bypassing load marking bit can also be not set in load predict segment 1120 to indicate a bypassing load. The predicted silent store needs to be marked as bypass. The marking of a silent store is accomplished by setting a bit in a store buffer (not shown). In another embodiment, the silent store marking bit can also be not set to indicate a silent store.

Please replace the title to read:

A3

Prediction of Issued Silent Store Operations for Allowing Subsequently Issued Loads to Bypass Unexecuted Silent Stores and Confirming the Bypass Upon Execution of the Stores.